Примечание 1: PN - Pin Number (LQFP-48 package)
Примечание 2: AF - Alternate Function type **(D - Default, *R - Remap*)**
Примечание 3: 5V - 5V-tolerant (FT - Full Tolerance, N - No tolerance)

Pin Name	*PN*	*Port*	*AF*	*5V*	*Default*				*Remap*			*Condition*
							USART					
USART1_RX	**31**	**PA10**	**D**	**FT**								**USART1_REMAP = 0**
USART1_TX	**30**	**PA9**	**D**	**FT**								**USART1_REMAP = 0**
USART1_CTS	32	PA11	D	FT	USBDM	CAN_RX						**USART1_REMAP = [0 or 1]**
USART1_RTS	33	PA12	D	FT	USBDP	CAN_TX						**USART1_REMAP = [0 or 1]**
USART1_CK	29	PA8	D	FT				MCO				**USART1_REMAP = [0 or 1]**
USART1_RX	***43***	***PB7***	***R***	***FT***	***I2C1_SDA***							***USART1_REMAP = 1***
USART1_TX	***42***	***PB6***	***R***	***FT***	***I2C1_SCL***							***USART1_REMAP = 1***
USART2_RX	**13**	**PA3**	**D**	**N**	**ADCx_IN3**							**USART2_REMAP = 0**
USART2_TX	**12**	**PA2**	**D**	**N**	**ADCx_IN2**							**USART2_REMAP = 0**
USART2_CTS	10	PA0	D	N	ADCx_IN0			WKUP				USART2_REMAP = 0
USART2_RTS	11	PA1	D	N	ADCx_IN1							USART2_REMAP = 0
USART2_CK	14	PA4	D	N	ADCx_IN4	SPI1_NSS		DAC1_OUT				USART2_REMAP = 0
USART3_RX	**22**	**PB11**	**D**	**FT**	**I2C2_SDA**							**USART3_REMAP[1:0] = 00**
USART3_TX	**21**	**PB10**	**D**	**FT**	**I2C2_SCL**						**CEC**	**USART3_REMAP[1:0] = 00**
USART3_CK	25	PB12	D	FT	I2C2_SMBA	SPI2_NSS						USART3_REMAP[1:0] = 00
USART3_CTS	26	PB13	D	FT		SPI2_SCK						USART3_REMAP[1:0] = 00
USART3_RTS	27	PB14	D	FT		SPI2_MISO						USART3_REMAP[1:0] = 00
							SPI					
SPI1_MOSI	**17**	**PA7**	**D**	**N**	**ADCx_IN7**							**SPI1_REMAP = 0**
SPI1_MISO	**16**	**PA6**	**D**	**N**	**ADCx_IN6**							**SPI1_REMAP = 0**
SPI1_SCK	**15**	**PA5**	**D**	**N**	**ADCx_IN5**			**DAC2_OUT**				**SPI1_REMAP = 0**
SPI1_NSS	**14**	**PA4**	**D**	**N**	**ADCx_IN4**	**USART2_CK**		**DAC1_OUT**				**SPI1_REMAP = 0**
SPI1_MOSI	***41***	***PB5***	***R***	***N***	***I2C1_SMBA***							***SPI1_REMAP = 1***
SPI1_MISO	***40***	***PB4***	***R***	***FT***	***NJTRST***				***PB4***			***SPI1_REMAP = 1***
SPI1_SCK	***39***	***PB3***	***R***	***FT***	***JTDO***				***PB3***		***TRACESWO***	***SPI1_REMAP = 1***
SPI1_NSS	***38***	***PA15***	***R***	***FT***	***JTDI***				***PA15***			***SPI1_REMAP = 1***
SPI2_MOSI	**28**	**PB15**	**D**	**FT**								No Condition
SPI2_MISO	**27**	**PB14**	**D**	**FT**		**USART3_RTS**						No Condition
SPI2_SCK	**26**	**PB13**	**D**	**FT**		**USART3_CTS**						No Condition
SPI2_NSS	**25**	**PB12**	**D**	**FT**	**I2C2_SMBA**	**USART3_CK**						No Condition

Pin Name	PN	Port	AF	5V	Default				Remap			Condition
							I2C					
I2C1_SCL	42	PB6	D	FT					USART1_TX			I2C1_REMAP = 0
I2C1_SDA	43	PB7	D	FT					USART1_RX			I2C1_REMAP = 0
I2C1_SMBA	41	PB5	D	N					SPI1_MOSI			I2C1_REMAP = [0 or 1]
I2C1_SCL	*45*	*PB8*	*R*	*FT*		*CEC*			*CAN_RX*			*I2C1_REMAP = 1*
I2C1_SDA	*46*	*PB9*	*R*	*FT*					*CAN_TX*			*I2C1_REMAP = 1*
I2C2_SCL	21	PB10	D	FT		USART3_TX					CEC	No Condition
I2C2_SDA	22	PB11	D	FT		USART3_RX						No Condition
I2C2_SMBA	25	PB12	D	FT	SPI2_NSS	USART3_CK						No Condition
							USB					
USBDM	32	PA11	D	FT	CAN_RX	USART1_CTS						No Condition
USBDP	33	PA12	D	FT	CAN_TX	USART1_RTS						No Condition
							CAN					
CAN_RX	32	PA11	D	FT	USBDM	USART1_CTS						CAN_REMAP[1:0] = 00
CAN_TX	33	PA12	D	FT	USBDP	USART1_RTS						CAN_REMAP[1:0] = 00
CAN_RX	*45*	*PB8*	*R*	*FT*		*CEC*			*I2C1_SCL*			*CAN_REMAP[1:0] = 10*
CAN_TX	*46*	*PB9*	*R*	*FT*					*I2C1_SDA*			*CAN_REMAP[1:0] = 10*
							ADC					
ADCx_IN0	10	PA0	D	N	WKUP	USART2_CTS						No Condition
ADCx_IN1	11	PA1	D	N		USART2_RTS						No Condition
ADCx_IN2	12	PA2	D	N		USART2_TX						No Condition
ADCx_IN3	13	PA3	D	N		USART2_RX						No Condition
ADCx_IN4	14	PA4	D	N	SPI1_NSS	USART2_CK		DAC1_OUT				No Condition
ADCx_IN5	15	PA5	D	N	SPI1_SCK			DAC2_OUT				No Condition
ADCx_IN6	16	PA6	D	N	SPI1_MISO							No Condition
ADCx_IN7	17	PA7	D	N	SPI1_MOSI							No Condition
ADCx_IN8	18	PB0	D	N								No Condition
ADCx_IN9	19	PB1	D	N								No Condition

Pin Name	PN	Port	AF	5V	Default				Remap			Condition
							TIMERS					
TIM1_CH1	29	PA8	D	FT		USART1_CK		MCO				TIM1_REMAP[1:0] = [00 or 01]
TIM1_CH2	30	PA9	D	FT		USART1_TX		TIM15_BKIN				TIM1_REMAP[1:0] = [00 or 01]
TIM1_CH3	31	PA10	D	FT		USART1_RX		TIM17_BKIN				TIM1_REMAP[1:0] = [00 or 01]
TIM1_CH4	32	PA11	D	FT	USBDM	USART1_CTS	CAN_RX					TIM1_REMAP[1:0] = [00 or 01]
TIM1_ETR	33	PA12	D	FT	USBDP	USART1_RTS	CAN_TX					TIM1_REMAP[1:0] = [00 or 01]
TIM1_BKIN	25	PB12	D	FT	SPI2_NSS	USART3_CK	I2C2_SMBA					TIM1_REMAP[1:0] = 00
TIM1_CH1N	26	PB13	D	FT	SPI2_SCK	USART3_CTS						TIM1_REMAP[1:0] = 00
TIM1_CH2N	27	PB14	D	FT	SPI2_MISO	USART3_RTS					TIM15_CH1	TIM1_REMAP[1:0] = 00
TIM1_CH3N	28	PB15	D	FT	SPI2_MOSI			TIM15_CH1N			TIM15_CH2	TIM1_REMAP[1:0] = 00
TIM1_BKIN	*16*	*PA6*	*R*	*N*	*SPI1_MISO*	*ADCx_IN6*	*TIM3_CH1*				*TIM16_CH1*	*TIM1_REMAP[1:0] = 01*
TIM1_CH1N	*17*	*PA7*	*R*	*N*	*SPI1_MOSI*	*ADCx_IN7*	*TIM3_CH2*				*TIM17_CH1*	*TIM1_REMAP[1:0] = 01*
TIM1_CH2N	*18*	*PB0*	*R*	*N*		*ADCx_IN8*	*TIM3_CH3*					*TIM1_REMAP[1:0] = 01*
TIM1_CH3N	*19*	*PB1*	*R*	*N*		*ADCx_IN9*	*TIM3_CH4*					*TIM1_REMAP[1:0] = 01*
TIM2_CH1_ETR	10	PA0	D	N	ADCx_IN0	USART2_CTS	WKUP					TIM2_REMAP[1:0] = [00 or 10]
TIM2_CH2	11	PA1	D	N	ADCx_IN1	USART2_RTS						TIM2_REMAP[1:0] = [00 or 10]
TIM2_CH3	12	PA2	D	N	ADCx_IN2	USART2_TX		TIM15_CH1				TIM2_REMAP[1:0] = [00 or 01]
TIM2_CH4	13	PA3	D	N	ADCx_IN3	USART2_RX		TIM15_CH2				TIM2_REMAP[1:0] = [00 or 01]
TIM2_CH1_ETR	*38*	*PA15*	*R*	*FT*	*JTDI*				*SPI1_NSS*	*PA15*		*TIM2_REMAP[1:0] = [01 or 11]*
TIM2_CH2	*39*	*PB3*	*R*	*FT*	*JTDO*				*SPI1_SCK*	*PB3*	*TRACESWO*	*TIM2_REMAP[1:0] = [01 or 11]*
TIM2_CH3	*21*	*PB10*	*R*	*FT*	*I2C2_SCL*	*USART3_TX*					*CEC*	*TIM2_REMAP[1:0] = [10 or 11]*
TIM2_CH4	*22*	*PB11*	*R*	*FT*	*I2C2_SDA*	*USART3_RX*						*TIM2_REMAP[1:0] = [10 or 11]*
TIM3_CH1	16	PA6	D	N	ADCx_IN6	SPI1_MISO				TIM1_BKIN	TIM16_CH1	TIM3_REMAP[1:0] = 00
TIM3_CH2	17	PA7	D	N	ADCx_IN7	SPI1_MOSI				TIM1_CH1N	TIM17_CH1	TIM3_REMAP[1:0] = 00
TIM3_CH3	18	PB0	D	N	ADCx_IN8					TIM1_CH2N		TIM3_REMAP[1:0] = [00 or 10]
TIM3_CH4	19	PB1	D	N	ADCx_IN9					TIM1_CH3N		TIM3_REMAP[1:0] = [00 or 10]
TIM3_CH1	*40*	*PB4*	*R*	*FT*	*NJTRST*				*SPI1_MISO*	*PB4*		*TIM3_REMAP[1:0] = 10*
TIM3_CH2	*41*	*PB5*	*R*	*N*		*I2C1_SMBA*		*TIM16_BKIN*	*SPI1_MOSI*			*TIM3_REMAP[1:0] = 10*
TIM4_CH1	42	PB6	D	FT		I2C1_SCL		TIM16_CH1N	USART1_TX			TIM4_REMAP = 0
TIM4_CH2	43	PB7	D	FT		I2C1_SDA		TIM17_CH1N	USART1_RX			TIM4_REMAP = 0
TIM4_CH3	45	PB8	D	FT			CEC	TIM16_CH1		I2C1_SCL	CAN_RX	TIM4_REMAP = 0
TIM4_CH4	46	PB9	D	FT				TIM17_CH1		I2C1_SDA	CAN_TX	TIM4_REMAP = 0

Pin Name	PN	Port	AF	5V	Default				Remap			Condition
							TIMERS					
TIM15_CH1	12	PA2	D	N	ADCx_IN2	USART2_TX	TIM2_CH3					TIM15_REMAP = 0
TIM15_CH2	13	PA3	D	N	ADCx_IN3	USART2_RX	TIM2_CH4					TIM15_REMAP = 0
TIM15_BKIN	30	PA9	D	FT		USART1_TX	TIM1_CH2					TIM15_REMAP = [0 or 1]
TIM15_CH1N	28	PB15	D	FT	SPI2_MOSI		TIM1_CH3N				TIM15_CH2	TIM15_REMAP = [0 or 1]
TIM15_CH1	*27*	*PB14*	*R*	*FT*	*SPI2_MISO*	*USART3_RTS*	*TIM1_CH2N*					*TIM15_REMAP = 1*
TIM15_CH2	*28*	*PB15*	*R*	*FT*	*SPI2_MOSI*		*TIM1_CH3N*	*TIM15_CH1N*				*TIM15_REMAP = 1*
TIM16_CH1	45	PB8	D	FT			TIM4_CH3	CEC	CAN_RX	I2C1_SCL		TIM16_REMAP = 0
TIM16_BKIN	41	PB5	D	N		I2C1_SMBA			SPI1_MOSI	TIM3_CH2		TIM16_REMAP = [0 or 1]
TIM16_CH1N	42	PB6	D	FT		I2C1_SCL	TIM4_CH1		USART1_TX			TIM16_REMAP = [0 or 1]
TIM16_CH1	*16*	*PA6*	*R*	*N*	*ADCx_IN6*	*SPI1_MISO*	*TIM3_CH1*			*TIM1_BKIN*		*TIM16_REMAP = 1*
TIM17_CH1	46	PB9	D	FT			TIM4_CH4		CAN_TX	I2C1_SDA		TIM17_REMAP = 0
TIM17_BKIN	31	PA10	D	FT		USART1_RX	TIM1_CH3					TIM17_REMAP = [0 or 1]
TIM17_CH1N	43	PB7	D	FT		I2C1_SDA	TIM4_CH2		USART1_RX			TIM17_REMAP = [0 or 1]
TIM17_CH1	*17*	*PA7*	*R*	*N*	*ADCx_IN7*	*SPI1_MOSI*	*TIM3_CH2*			*TIM1_CH1N*		*TIM17_REMAP = 1*